

# Towards a Trustworthy Semantics-Based Language Framework via Proof Generation

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**Abstract.** We pursue the vision of an *ideal language framework*, where programming language designers only need to define the formal *syntax* and *semantics* of their languages, and all language tools are automatically generated by the framework. Due to the complexity of such a language framework, it is a big challenge to ensure its trustworthiness and to establish the correctness of the autogenerated language tools. In this paper, we propose an innovative approach based on *proof generation*. The key idea is to generate proof objects as correctness certificates for each individual task that the language tools conduct, on a case-by-case basis, and use a trustworthy proof checker to check the proof objects. This way, we avoid formally verifying the entire framework, which is practically impossible, and thus can make the language framework both *practical* and *trustworthy*. As a first step, we formalize program execution as mathematical proofs and generate their complete proof objects. The experimental result shows that the performance of our proof object generation and proof checking is very promising.

**Keywords:** Semantic framework · Proof generation · Proof checking.

## 1 Introduction

Unlike natural languages that allow vagueness and ambiguity, programming languages must be precise and unambiguous. Only with rigorous definitions of programming languages, called the *formal semantics*, can we guarantee the reliability, safety, and security of computing systems.

Our vision is thus an *ideal language framework* based on the formal semantics of programming languages. Shown in Figure 1, an ideal language framework is one where language designers only need to define the formal syntax and semantics of their language, and all language tools are automatically generated by the framework. The *correctness* of these language tools is established by generating complete mathematical proofs as certificates that can be automatically machine-checked by a trustworthy proof checker.

The  $\mathbb{K}$  language framework (<https://kframework.org>) is in pursuit of the above ideal vision. It provides a simple and intuitive front end language (i.e.,

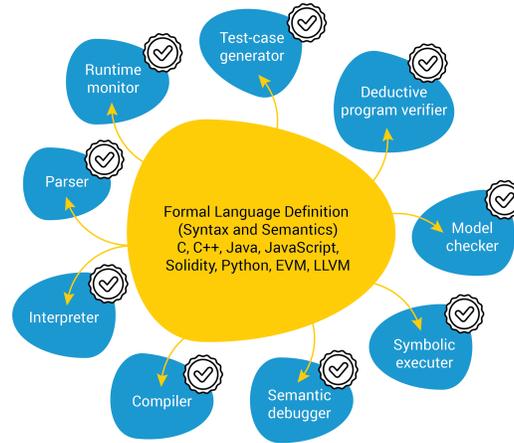


Fig. 1: An ideal language framework vision; language tools are autogenerated, with machine-checkable mathematical proofs  as correctness certificates.

a meta-language) for language designers to define the formal syntax and semantics of other programming languages. From such a formal language definition, the framework automatically generates a set of language tools, including a parser, an interpreter, a deductive verifier, a program equivalence checker, among many others [9,24].  $\mathbb{K}$  has obtained much success in practice, and has been used to define the complete executable formal semantics of many real-world languages, such as C [12], Java [2], JavaScript [21], Python [13], Ethereum virtual machines byte code [15], and x86-64 [10], from which their implementations and formal analysis tools are automatically generated. Some commercial products [14,18] are powered by these autogenerated implementations and/or tools.

What is *missing* in  $\mathbb{K}$  (compared to the ideal vision in Figure 1) is its ability to generate proof objects as correctness certificates. The current  $\mathbb{K}$  implementation is a complex artifact with over 500,000 lines of code written in 4 programming languages, with new code committed on a weekly basis. Its code base includes complex data structures, algorithms, optimizations, and heuristics to support the various features such as defining formal language syntax using BNF grammar, defining computation configurations as constructor terms, defining formal semantics using rewrite rules, specifying arbitrary evaluation strategies, and defining the binding behaviors of binders (Section 3). The large code base and rich features make it challenging to formally verify the correctness of  $\mathbb{K}$ .

Our **main contribution** is the proposal of a *practical approach* to establishing the correctness of a complex language framework, such as  $\mathbb{K}$ , via *proof object generation*. Our approach consists of the following main components:

1. A small *logical foundation* of  $\mathbb{K}$ ;
2. *Proof parameters* that are provided by  $\mathbb{K}$  as the hints for proof generation;
3. A *proof object generator* that generates *proof objects* from proof parameters;

4. A fast and trustworthy third-party *proof checker* that verifies proof objects.

The key idea that makes our approach practical is that we establish the correctness not for the entire framework, but for each individual language tasks that it conducts, on a case-by-case basis. This idea is not limited to  $\mathbb{K}$  but also applicable to the existing language frameworks and/or formal semantics approaches.

As a first step, we formalize *program execution* as mathematical proofs and generate their complete proof objects. The experimental result (Table 1) shows promising performance of the proof object generation and proof checking. For example, for a 100-step program execution trace, its complete proof object has 1.6 million lines of code that takes only 5.6 seconds to proof-check.

We organize the rest of the paper as follows. We give an overview of our approach in Section 2. We introduce  $\mathbb{K}$  and discuss the generation of proof parameters in Section 3. We discuss *matching logic*—the logical foundation of  $\mathbb{K}$ —in Section 4. We then compile  $\mathbb{K}$  to matching logic in Section 5, and discuss *proof object generation* in Section 6. We discuss the limitations of our current implementation and show the experiment results in Sections 7 and 8, respectively. Finally, we discuss related work in Section 9 and conclude the paper in Section 10.

## 2 Our Approach Overview

We give an overview of our approach via the following four main components: (1) a logical foundation of  $\mathbb{K}$ , (2) proof parameters, (3) proof object generation, and (4) a trustworthy proof checker.

**Logical Foundation of  $\mathbb{K}$ .** Our approach is based on *matching logic* [22,5]. Matching logic is the *logical foundation* of  $\mathbb{K}$ , in the following sense:

1. The  $\mathbb{K}$  definition (i.e., the language definition in Figure 1) of a programming language  $L$  corresponds to a *matching logic theory*  $\Gamma^L$ , which, roughly speaking, consists of a set of logical symbols that represents the formal syntax of  $L$ , and a set of logical axioms that specify the formal semantics.
2. All language tools in Figure 1 and all language tasks that  $\mathbb{K}$  conducts are formally specified by matching logic formulas. For example, *program execution* is specified (in our approach) by the following matching logic formula:

$$\varphi_{init} \Rightarrow \varphi_{final} \tag{1}$$

where  $\varphi_{init}$  is the formula that specifies the initial state of the execution,  $\varphi_{final}$  specifies the final state, and “ $\Rightarrow$ ” states the rewriting/reachability relation between states (see Section 5.1).

3. There exists a matching logic *proof system* that defines the provability relation  $\vdash$  between theories and formulas. For example, the correctness of the above execution from  $\varphi_{init}$  to  $\varphi_{final}$  is witnessed by the formal proof:

$$\Gamma^L \vdash \varphi_{init} \Rightarrow \varphi_{final} \tag{2}$$

Therefore, matching logic is the logical foundation of  $\mathbb{K}$ . The *correctness* of  $\mathbb{K}$  conducting one language task is reduced to the *existence of a formal proof* in matching logic. Such formal proofs are encoded as proof objects, discussed below.

**Proof Parameters.** A proof parameter is the necessary information that  $\mathbb{K}$  should provide to help generate proof objects. For program execution, such as Equation (2), the proof parameter includes the following information:

- the complete execution trace  $\varphi_0, \varphi_1, \dots, \varphi_n$ , where  $\varphi_0 \equiv \varphi_{init}$  and  $\varphi_n \equiv \varphi_{final}$ ; we call  $\varphi_0, \dots, \varphi_n$  the intermediate *snapshots* of the execution;
- for each step from  $\varphi_i$  to  $\varphi_{i+1}$ , the *rewriting information* that consists of the rewrite/semantic rule  $\varphi_{lhs} \Rightarrow \varphi_{rhs}$  that is applied, and the corresponding substitution  $\theta$  such that  $\varphi_{lhs}\theta \equiv \varphi_i$ .

In other words, a proof parameter of a program execution trace contains the complete information about how such an execution is carried out by  $\mathbb{K}$ . The proof parameter, once generated by  $\mathbb{K}$ , is passed to the proof object generator to generate the corresponding proof object, discussed below.

**Proof Object Generation.** In our approach, a proof object is an encoding of matching logic formal proofs, such as Equation (2). Proof objects are generated by a proof object generator from the proof parameters provided by  $\mathbb{K}$ . At a high level, a proof object for program execution, such as Equation (2), consists of:

1. the formalization of matching logic and its provability relation  $\vdash$ ;
2. the formalization of the formal semantics  $\Gamma^L$  as a logical theory, which includes axioms that specify the rewrite/semantic rules  $\varphi_{lhs} \Rightarrow \varphi_{rhs}$ ;
3. the formal proofs of all one-step executions, i.e.,  $\Gamma^L \vdash \varphi_i \Rightarrow \varphi_{i+1}$  for all  $i$ ;
4. the formal proof of the final proof goal  $\Gamma^L \vdash \varphi_{init} \Rightarrow \varphi_{final}$ .

Our proof objects have a *linear structure*, which implies a nice separation of concerns. Indeed, Item 1 is only about matching logic and is *not specific* to any programming languages/language tasks, so we only need to develop and proof-check it *once and for all*. Item 2 is specific to the language semantics  $\Gamma^L$  but is independent of the actual program executions, so it can be reused in the proof objects of various language executions for the same programming language  $L$ .

**A Trustworthy Proof Checker.** A proof checker is a small program that checks whether the formal proofs encoded in a proof object are correct. The proof checker is the main trust base of our work. In this paper, we use Metamath [20]—a third-party proof checking tool that is simple, fast, and trustworthy—to formalize matching logic and encode its formal proofs.

```

1  module IMP-SYNTAX
2  imports DOMAINS-SYNTAX
3  syntax Exp ::=
4    Int
5    | Id
6    | Exp "+" Exp [left, strict]
7    | Exp "-" Exp [left, strict]
8    | "(" Exp ")" [bracket]
9  syntax Stmt ::=
10   Id "=" Exp ";" [strict(2)]
11   | "if" "(" Exp ")"
12     Stmt Stmt [strict(1)]
13   | "while" "(" Exp ")" Stmt
14   | "{" Stmt "}" [bracket]
15   | "{" "}"
16   > Stmt Stmt [left, strict(1)]
17  syntax Pgm ::= "int" Ids ";" Stmt
18  syntax Ids ::= List{Id, ","}
19  endmodule

20 module IMP imports IMP-SYNTAX
21 imports DOMAINS
22 syntax KResult ::= Int
23 configuration
24   <T> <k> $Pgm:Pgm </k>
25   <state> .Map </state> </T>
26 rule <k> X:Id => I ...</k>
27   <state>... X |-> I ...</state>
28 rule I1 + I2 => I1 +Int I2
29 rule I1 - I2 => I1 -Int I2
30 rule <k> X = I:Int => I ...</k>
31   <state>... X |-> ( _ => I) ...</state>
32 rule {} S:Stmt => S
33 rule if(I) S _ => S requires I /=Int 0
34 rule if(0) _ S => S
35 rule while(B) S => if(B) {S while(B) S} {}
36 rule <k> int (X, Xs => Xs) ; S </k>
37   <state>... ( . => X |-> 0) </state>
38 rule int .Ids ; S => S
39 endmodule

```

Fig. 2: The complete  $\mathbb{K}$  formal definition of an imperative language IMP.

**Summary.** Our approach to establishing the correctness of  $\mathbb{K}$  is based on its logical foundation—matching logic. We formalize language semantics as logical theories, and program executions as formulas and proof goals, whose proof objects are automatically generated and proof-checked. Our proof objects have a linear structure that allows easy reuse of their components. The key characteristics of our logical-based approach are the following:

- It is *faithful* to the real  $\mathbb{K}$  implementation because proof objects are generated from proof parameters, which include all execution snapshots and the actual rewriting information, provided by  $\mathbb{K}$ .
- It is *practical* because proof objects are generated for each program executions on a case-by-case bases, avoiding the verification of the entire  $\mathbb{K}$ .
- It is *trustworthy* because the autogenerated proof objects are checked using the trustworthy third-party Metamath proof checker.

### 3 $\mathbb{K}$ Framework and Generation of Proof Parameters

#### 3.1 $\mathbb{K}$ Overview

$\mathbb{K}$  is an effort in realizing the ideal language framework vision in Figure 1. An easy way to understand  $\mathbb{K}$  is to look at it as a meta-language that can define other programming languages. In Figure 2, we show an example  $\mathbb{K}$  language definition of an imperative language IMP. In the 39-line definition, we *completely* define the formal syntax and the (executable) formal semantics of IMP, using a front end language that is easy to understand. From this language definition,  $\mathbb{K}$  can generate all language tools for IMP, including its parser, interpreter, verifier, etc.

We use IMP as an example to illustrate the main  $\mathbb{K}$  features. There are two *modules*: `IMP-SYNTAX` defines the syntax and `IMP` defines the semantics using rewrite rules. Syntax is defined as BNF grammars. The keyword `syntax` leads

production rules that can have attributes that specify the additional syntactic and/or semantic information. For example, the syntax of `if`-statements is defined in lines 11-12 and has the attribute `[strict(1)]`, meaning that the evaluation order is strict in the first argument, i.e., the condition of an `if`-statement.

In the module `IMP`, we define the *configurations* of `IMP` and its formal semantics. A configuration (lines 23-25) is a constructor term that has all semantic information needed to execute programs. `IMP` configurations are simple, consisting of the `IMP` code and a program state that maps variables to values. We organize configurations using (*semantic*) *cells*: `</k>` is the cell of `IMP` code and `</state>` is the cell of program states. In the initial configuration (lines 24-25), `</state>` is empty and `</k>` contains the `IMP` program that we pass to  $\mathbb{K}$  for execution (represented by the special  $\mathbb{K}$  variable `$PGM`).

We define formal semantics using *rewrite rules*. In lines 26-27, we define the semantics of variable lookup, where we match on a variable `x` in the `</k>` cell and look up its value `I` in the `</state>` cell, by matching on the binding `x ↦ I`. Then, we rewrite `x` to `I`, denoted by `x ⇒ I` in the `</k>` cell in line 26. Rewrite rules in  $\mathbb{K}$  are similar to those in the rewrite engines such as Maude [7].

**A Running Example.** `IMP` is too complex as a running example so we introduce a simpler one: `TWO-COUNTERS`. Although simple, `TWO-COUNTERS` still uses the core features of defining formal syntax as grammars and formal semantics as rewrite rules.

```

1  module TWO-COUNTERS
2  imports INT
3  syntax State ::= "<" Int "," Int ">"
4  configuration <T> $PGM:State </T>
5  rule <M, N> => <M -Int 1, N +Int M>
6      requires M >Int 0
7  endmodule

```

Fig. 3: Running example `TWO-COUNTERS`.

`TWO-COUNTERS` is a tiny language that defines a state machine with two counters. Its computation configuration is simply a pair  $\langle m, n \rangle$  of two integers  $m$  and  $n$ , and its semantics is defined by the following (conditional) rewrite rule:

$$\langle m, n \rangle \Rightarrow \langle m - 1, n + m \rangle \quad \text{if } m > 0 \quad (3)$$

Therefore, `TWO-COUNTERS` adds  $n$  by  $m$  and reduces  $m$  by 1. Starting from the initial state  $\langle m, 0 \rangle$ , `TWO-COUNTERS` carries out  $m$  execution steps and terminates at the final state  $\langle 0, m(m+1)/2 \rangle$ , where  $m(m+1)/2 = m + (m-1) + \dots + 1$ .

### 3.2 Program Execution and Proof Parameters

In the following, we show a concrete program execution trace of `TWO-COUNTERS` starting from the initial state  $\langle 100, 0 \rangle$ :

$$\langle 100, 0 \rangle, \langle 99, 100 \rangle, \langle 98, 199 \rangle, \dots, \langle 1, 5049 \rangle, \langle 0, 5050 \rangle \quad (4)$$

To make  $\mathbb{K}$  generate the above execution trace, we need to follow these steps:

1. Prepare the initial state  $\langle 100, 0 \rangle$  in a source file, say `100.two-counters`.
2. Compile the formal semantics `TWO-COUNTERS` into a matching logic theory, explained in Section 5.

3. Use the  $\mathbb{K}$  execution tool `krun` and pass the source file to it:

```
$ krun 100.two-counters --depth N
```

The option `--depth N` tells  $\mathbb{K}$  to execute for `N` steps and output the (intermediate) snapshot. By letting `N` be `1, 2, ...`, we collect all snapshots in Equation (4).

The *proof parameter* of Equation (4) includes the additional rewriting information for each execution step. That is, we need to know the rewrite rule that is applied and the corresponding substitution. In `TWO-COUNTERS`, there is only one rewrite rule, and the substitution can be easily obtained by pattern matching, where we simply match the snapshot with the left-hand side of the rewrite rule.

Note that we regard  $\mathbb{K}$  as a “black box”. We are not interested in its complex internal algorithms. Instead, we hide such complexity by letting  $\mathbb{K}$  generate proof parameters that include enough information for proof object generation. This way, we create a separation of concerns between  $\mathbb{K}$  and proof object generation.  $\mathbb{K}$  can aim at optimizing the performance of the autogenerated language tools, *without* making proof object generation more complex.

## 4 Matching Logic and Its Formalization

We review the syntax and proof system of matching logic—the logical foundation of  $\mathbb{K}$ . Then, we discuss its formalization, which is our main technical contribution and is a critical component of the proof objects we generate for  $\mathbb{K}$  (see Section 2).

### 4.1 Matching Logic Overview

Matching logic was proposed in [23] as a means to specify and reason about programs compactly and modularly. The key concept is its formulas, called *patterns*, which are used to specify program syntax and semantics in a uniform way. Matching logic is known for its simplicity and rich expressiveness. In [22,5,6,4], the authors developed matching logic theories that capture FOL, FOL-lfp, separation logic, modal logic, temporal logics, Hoare logic,  $\lambda$ -calculus, type systems, etc. In Section 5, we discuss the matching logic theories that capture  $\mathbb{K}$ .

The *syntax* of matching logic is parametric in two sets of variables  $EV$  and  $SV$ . We call  $EV$  the set of *element variables*, denoted  $x, y, \dots$ , and  $SV$  the set of *set variables*, denoted  $X, Y, \dots$ .

**Definition 1.** A (matching logic) signature  $\Sigma$  is a set of (constant) symbols. The set of  $\Sigma$ -patterns, denoted  $\text{PATTERN}(\Sigma)$ , is inductively defined as follows:

$$\varphi ::= x \mid X \mid \sigma \mid \varphi_1 \varphi_2 \mid \perp \mid \varphi_1 \rightarrow \varphi_2 \mid \exists x. \varphi \mid \mu X. \varphi$$

where in  $\mu X. \varphi$  we require that  $\varphi$  has no negative occurrences of  $X$ .

Thus, element variables, set variables, and symbols are patterns.  $\varphi_1 \varphi_2$  is a pattern, called *application*, where the first argument is applied to the second. We

$x[\psi/x] \equiv \psi$	$y[\psi/x] \equiv y$ if $y \neq x$
$\sigma[\psi/x] \equiv \sigma$	$(\varphi_1 \rightarrow \varphi_2)[\psi/x] \equiv \varphi[\psi_1/x] \rightarrow \varphi_2[\psi/x]$
$\perp[\psi/x] \equiv \perp$	$(\varphi_1 \varphi_2)[\psi/x] \equiv (\varphi_1[\psi/x]) (\varphi_2[\psi/x])$
$(\exists x. \varphi)[\psi/x] \equiv \exists x. \varphi$	$(\exists x. \varphi)[\psi/y] \equiv \exists z. \varphi[z/x][\psi/y]$ for fresh $z$
	$(\mu X. \varphi)[\psi/x] \equiv \mu Z. \varphi[Z/X][\psi/x]$ for fresh $Z$

Fig. 4: Capture-free substitution are defined in the usual way and formalized later in Section 4.2 as a part of our proof objects.

have propositional connectives  $\perp$  and  $\varphi_1 \rightarrow \varphi_2$ , existential quantification  $\exists x. \varphi$ , and the least fixpoints  $\mu X. \varphi$ , from which the following *notations* are defined:

$$\begin{array}{lll} \neg\varphi \equiv \varphi \rightarrow \perp & \top \equiv \neg\perp & \varphi_1 \wedge \varphi_2 \equiv \neg(\neg\varphi_1 \vee \neg\varphi_2) \\ \varphi_1 \vee \varphi_2 \equiv \neg\varphi_1 \rightarrow \varphi_2 & \forall x. \varphi \equiv \neg\exists x. \neg\varphi & \nu X. \varphi \equiv \neg\mu X. \neg\varphi[\neg X/X] \end{array}$$

We use  $\text{FV}(\varphi)$  to denote the free variables of  $\varphi$ , and  $\varphi[\psi/x]$  and  $\varphi[\psi/X]$  to denote capture-free substitution. Their (usual) definitions are listed in Figure 4.

Matching logic has a *pattern matching semantics*, where a pattern  $\varphi$  is interpreted as the set of elements that match it. For example,  $\varphi_1 \wedge \varphi_2$  is the pattern that is matched by those matching both  $\varphi_1$  and  $\varphi_2$ . Matching logic semantics is not needed for proof object generation, so we exile it to [22,5].

We show the *matching logic proof system* in Figure 5, which defines the provability relation, written  $\Gamma \vdash \varphi$ , meaning that  $\varphi$  can be proved using the proof system, with patterns in  $\Gamma$  added as additional axioms. We call  $\Gamma$  a *matching logic theory*. The proof system is a main component of proof objects. To understand it, we first need to define *application contexts*.

**Definition 2.** A context is a pattern  $C$  with a hole variable  $\square$ . We write  $C[\varphi] \equiv C[\varphi/\square]$  as the result of context plugging. We call  $C$  an application context, if

1.  $C \equiv \square$  is the identity context; or
2.  $C \equiv \varphi C'$  or  $C \equiv C' \varphi$ , where  $C'$  is an application context and  $\square \notin \text{FV}(\varphi)$ .

That is, the path from the root to  $\square$  in  $C$  has only applications.

The proof rules are sound and can be divided into 4 categories: FOL reasoning, frame reasoning, fixpoint reasoning, and some technical rules. The FOL reasoning rules provide (complete) FOL reasoning (see, e.g., [25]). The frame reasoning rules state that application contexts are commutative with disjunctive connectives such as  $\vee$  and  $\exists$ . The fixpoint reasoning rules support the standard fixpoint reasoning as in modal  $\mu$ -calculus [17]. The technical proof rules are needed for some completeness results (see [5] for details).

## 4.2 Formalizing Matching Logic

We discuss the formalization of matching logic, which is our first main contribution and forms an important component in our proof objects (see Section 2).

FOL Rules	{	(Propositional 1) $\varphi \rightarrow (\psi \rightarrow \varphi)$
		(Propositional 2) $(\varphi \rightarrow (\psi \rightarrow \theta)) \rightarrow ((\varphi \rightarrow \psi) \rightarrow (\varphi \rightarrow \theta))$
		(Propositional 3) $((\varphi \rightarrow \perp) \rightarrow \perp) \rightarrow \varphi$
		(Modus Ponens) $\frac{\varphi \quad \varphi \rightarrow \psi}{\psi}$
		( $\exists$ -Quantifier) $\varphi[y/x] \rightarrow \exists x. \varphi$
		( $\exists$ -Generalization) $\frac{\varphi \rightarrow \psi}{(\exists x. \varphi) \rightarrow \psi} \quad x \notin FV(\psi)$
Frame Rules	{	(Propagation $_{\perp}$ ) $C[\perp] \rightarrow \perp$
		(Propagation $_{\vee}$ ) $C[\varphi \vee \psi] \rightarrow C[\varphi] \vee C[\psi]$
		(Propagation $_{\exists}$ ) $C[\exists x. \varphi] \rightarrow \exists x. C[\varphi]$ with $x \notin FV(C)$
		(Framing) $\frac{\varphi \rightarrow \psi}{C[\varphi] \rightarrow C[\psi]}$
Fixpoint Rules	{	(Substitution) $\frac{\varphi}{\varphi[\psi/X]}$
		(Prefixpoint) $\varphi[(\mu X. \varphi)/X] \rightarrow \mu X. \varphi$
		(Knaster-Tarski) $\frac{\varphi[\psi/X] \rightarrow \psi}{(\mu X. \varphi) \rightarrow \psi}$
Technical Rules	{	(Existence) $\exists x. x$
		(Singleton) $\neg(C_1[x \wedge \varphi] \wedge C_2[x \wedge \neg\varphi])$

Fig. 5: Matching logic proof system (where  $C, C_1, C_2$  are application contexts).

Metamath [20] is a tiny language to state abstract mathematics and their proofs in a machine-checkable style. In our work, we use Metamath to formalize matching logic and to encode our proof objects. We choose Metamath for its simplicity and fast proof checking: Metamath proof checkers are often hundreds lines of code and can proof-check thousands of theorems in a second.

Our formalization follows closely Section 4.1. We formalize the syntax of patterns and the proof system. We also need to formalize some metalevel operations such as free variables and capture-free substitution. An *innovative* contribution is a generic way to handling *notations* (such as  $\neg$  and  $\wedge$ ) in matching logic. The resulting formalization has only 245 lines of code, which we show in [16]. This formalization of matching logic is the main trust base of our proof objects.

**Metamath Overview.** We use an extract of our formalization of matching logic (Figure 6) to explain the basic concepts in Metamath. At a high level, a Metamath source file consists of a list of *statements*. The main ones are:

1. *constant statements* ( `$c` ) that declare Metamath constants;

```

1  $c \imp ( ) #Pattern |- $.
2
3  $v ph1 ph2 ph3 $.
4  ph1-is-pattern $f #Pattern ph1 $.
5  ph2-is-pattern $f #Pattern ph2 $.
6  ph3-is-pattern $f #Pattern ph3 $.
7  imp-is-pattern
8  $a #Pattern ( \imp ph1 ph2 ) $.
9
10 axiom-1
11 $a |- ( \imp ph1 ( \imp ph2 ph1 ) ) $.
12
13 axiom-2
14 $a |- ( \imp ( \imp ph1 ( \imp ph2 ph3 ) )
15           ( \imp ( \imp ph1 ph2 )
16                 ( \imp ph1 ph3 ) ) ) $.
17
18 ${
19 rule-mp.0 $e |- ( \imp ph1 ph2 ) $.
20 rule-mp.1 $e |- ph1 $.
21 rule-mp $a |- ph2 $.
22 $}

23 imp-refl $p |- ( \imp ph1 ph1 )
24 $=
25 ph1-is-pattern ph1-is-pattern
26 ph1-is-pattern imp-is-pattern
27 imp-is-pattern ph1-is-pattern
28 ph1-is-pattern imp-is-pattern
29 ph1-is-pattern ph1-is-pattern
30 ph1-is-pattern imp-is-pattern
31 ph1-is-pattern imp-is-pattern
32 imp-is-pattern ph1-is-pattern
33 ph1-is-pattern ph1-is-pattern
34 imp-is-pattern imp-is-pattern
35 ph1-is-pattern ph1-is-pattern
36 imp-is-pattern imp-is-pattern
37 ph1-is-pattern ph1-is-pattern
38 ph1-is-pattern imp-is-pattern
39 ph1-is-pattern axiom-2
40 ph1-is-pattern ph1-is-pattern
41 ph1-is-pattern imp-is-pattern
42 axiom-1 rule-mp ph1-is-pattern
43 ph1-is-pattern axiom-1 rule-mp
44 $.

```

Fig. 6: An extract of the Metamath formalization of matching logic.

2. *variable statements* ( `$v` ) that declare Metamath variables, and *floating statements* ( `$f` ) that declare their intended ranges;
3. *axiomatic statements* ( `$a` ) that declare Metamath axioms, which can be associated with some *essential statements* ( `$e` ) that declare the premises;
4. *provable statements* ( `$p` ) that states a Metamath theorem and its proof.

Figure 6 defines the fragment of matching logic with only implications. We declare five constants in a row in line 1, where `\imp`, `(`, and `)` build the syntax, `#Pattern` is the type of patterns, and `|-` is the provability relation. We declare three metavariables of patterns in lines 3-6, and the syntax of implication  $\varphi_1 \rightarrow \varphi_2$  as `( \imp ph1 ph2 )` in line 7. Then, we define matching logic proof rules as Metamath axioms. For example, lines 18-22 define the rule (Modus Ponens).

In line 23, we show an example (meta-)theorem and its formal proof in Metamath. The theorem states that  $\vdash \varphi_1 \rightarrow \varphi_1$  holds, and its proof (lines 25-43) is a sequence of labels referring to the previous axiomatic/provable statements.

Metamath proofs are very easy to proof-check, which is why we use it in our work. The proof checker reads the labels in order and push them to a *proof stack*  $S$ , which is initially empty. When a label  $l$  is read, the checker pops its premise statements from  $S$  and pushes  $l$  itself. When all labels are consumed, the checker checks whether  $S$  has exactly one statement, which should be the original proof goal. If so, the proof is checked. Otherwise, it fails.

As an example, we look at the first 5 labels of the proof in Figure 6, line 25:

```

// Initially, the proof stack S is empty
ph1-is-pattern // S = [ #Pattern ph1 ]
ph1-is-pattern // S = [ #Pattern ph1 ; #Pattern ph1 ]
ph1-is-pattern // S = [ #Pattern ph1 ; #Pattern ph1 ; #Pattern ph1 ]
imp-is-pattern // S = [ #Pattern ph1 ; #Pattern ( \imp ph1 ph1 ) ]
imp-is-pattern // S = [ #Pattern ( \imp ph1 ( \imp ph1 ph1 ) ) ]

```

where we show the stack status in comments. The first label `ph1-is-pattern` refers to a `$f`-statement without premises, so nothing is popped off, and the corresponding statement `#Pattern ph1` is pushed to the stack. The same happens, for the second and third labels. The fourth label `imp-is-pattern` refers to a `$a`-statement with two metavariables of patterns, and thus has 2 premises. Therefore, the top two statements in  $S$  are popped off, and the corresponding conclusion `#Pattern ( \imp ph1 ph1 )` is pushed to  $S$ . The last label does the same, popping off two premises and pushing `#Pattern ( \imp ph1 ( \imp ph1 ph1 ) )` to  $S$ . Thus, these five proof steps prove the *wellformedness* of  $\varphi_1 \rightarrow (\varphi_1 \rightarrow \varphi_1)$ .

**Formalizing Matching Logic Syntax.** Now, we go through the formalization of matching logic and emphasize some highlights. See [22,5,6] for full detail.

The syntax of patterns is formalized below, following Definition 1:

```
$c \bot \imp \app \exists \mu ( ) $.
var-is-pattern    $a #Pattern xX $.
symbol-is-pattern $a #Pattern sg0 $.
bot-is-pattern    $a #Pattern \bot $.
imp-is-pattern    $a #Pattern ( \imp ph0 ph1 ) $.
app-is-pattern    $a #Pattern ( \app ph0 ph1 ) $.
exists-is-pattern $a #Pattern ( \exists x ph0 ) $.
${ mu-is-pattern.0 $e #Positive X ph0 $.
  mu-is-pattern   $a #Pattern ( \mu X ph0 ) $. }
```

Note that we omit the declarations of metavariables (such as `xX`, `sg0`, ...) because their meaning can be easily inferred. The only nontrivial case above is `mu-is-pattern`, where we require that `ph0` is positive in `x`, discussed below.

**Metalevel Assertions.** To formalize matching logic, we need the following metalevel operations and/or assertions:

1. positive (and negative) occurrences of variables;
2. free variables;
3. capture-free substitution;
4. application contexts;
5. notations.

Item 1 is needed to define the syntax of  $\mu X. \varphi$ , while Items 2-5 are needed to define the proof system (Figure 5). Here, we show how to define capture-free substitution as an example. Notations are discussed in the next section.

To formalize capture-free substitution, we first define a Metamath constant

```
$c #Substitution $.
```

that serves as an assertion symbol: `#Substitution ph ph' ph" xX` holds iff  $\text{ph} \equiv \text{ph}' [\text{ph}'' / \text{xX}]$ . Then, we can define substitution following Figure 4. The only nontrivial case is when `ph'` is  $\exists x. \varphi$  or  $\mu X. \varphi$ , in which case  $\alpha$ -renaming is required to avoid variable capture. We show the case when `ph'` is  $\exists x. \varphi$  below:

```

substitution-exists-shadowed
  $a #Substitution ( \exists x ph1 ) ( \exists x ph1 ) ph0 x $.
${ $d xX x $.
  $d y ph0 $.
  substitution-exists.0 $e #Substitution ph2 ph1 y x $.
  substitution-exists.1 $e #Substitution ph3 ph2 ph0 xX $.
substitution-exists
  $a #Substitution ( \exists y ph3 ) ( \exists x ph1 ) ph0 xX $. $}

```

There are two cases, as expected from Figure 4. `substitution-exists-shadowed` is when the substitution is shadowed. `substitution-exists` is the general case, where we first rename `x` to a fresh variable `y` and then continue the substitution. The `$d`-statements state that the substitution is not shadowed and `y` is fresh.

**Supporting Notations.** Notations (e.g.,  $\neg$  and  $\wedge$ ) play an important role in matching logic. Many proof rules such as (Propagation<sub>v</sub>) and (Singleton) use notations (see Figure 5). However, Metamath has no built-in support for notations. To define a notation, say  $\neg\varphi \equiv \varphi \rightarrow \perp$ , we need to (1) declare a constant `\not` and add it to the pattern syntax; (2) define the equivalence relation  $\neg\varphi \equiv \varphi \rightarrow \perp$ ; and (3) add a new case for `\not` to *every metalevel assertions*. While (1) and (2) are reasonable, we want to avoid (3) because there are many metalevel assertions and thus it creates duplication.

Therefore, we implement an innovative and generic method that allows us to define *any notations* in a compact way. Our method is to declare a new constant `#Notation` and use it to capture the *congruence relation of sugaring/desugaring*. Using `#Notation`, it takes only three lines to define the notation  $\neg\varphi \equiv \varphi \rightarrow \perp$ :

```

$c \not $.
not-is-pattern $a #Pattern ( \not ph0 ) $.
not-is-sugar  $a #Notation ( \not ph0 ) ( \imp ph0 \bot ) $.

```

To make the above work, we need to state that `#Notation` is a congruence relation with respect to the syntax of patterns and all the other metalevel assertions. Firstly, we state that it is reflexive, symmetric, and transitive:

```

notation-reflexivity $a #Notation ph0 ph0 $.
${ notation-symmetry.0 $e #Notation ph0 ph1 $.
  notation-symmetry  $a #Notation ph1 ph0 $. $}
${ notation-transitivity.0 $e #Notation ph0 ph1 $.
  notation-transitivity.1 $e #Notation ph1 ph2 $.
  notation-transitivity  $a #Notation ph0 ph2 $. $}

```

And the following is an example where we state that `#Notation` is a congruence with respect to provability:

```

${ notation-provability.0 $e #Notation ph0 ph1 $.
  notation-provability.1 $e |- ph0 $.
  notation-provability  $a |- ph1 $. $}

```

This way, we only need a *fixed* number of statements that state that `#Notation` is a congruence, making it more compact and less duplicated to define notations.

**Formalizing Proof System.** With metalevel assertions and notations, it is now straightforward to formalize matching logic proof rules. We have seen the formalization of (Modus Ponens) in Figure 6. In the following, we formalize the fixpoint proof rule (Kanaster-Tarski), whose premises use capture-free substitution:

```
{ rule-kt.0 $e #Substitution ph0 ph1 ph2 X $.
  rule-kt.1 $e |- ( \imp ph0 ph2 ) $.
  rule-kt $a |- ( \imp ( \mu X ph1 ) ph2 ) $. }
```

## 5 Compiling $\mathbb{K}$ into Matching Logic

To execute programs using  $\mathbb{K}$ , we need to compile the  $\mathbb{K}$  language definition for language  $L$  into a matching logic theory, written  $\Gamma^L$  (see Section 3.2). In this section, we discuss this compilation process and show how to formalize  $\Gamma^L$ .

### 5.1 Basic Matching Logic Theories

Firstly, we discuss the basic matching logic theories that are required by  $\Gamma^L$ . We discuss the theories of equality, sorts (and sorted functions), and rewriting.

**Theory of Equality.** By equality, we mean a (predicate) pattern  $\varphi_1 = \varphi_2$  that holds (i.e., equals to  $\top$ ) iff  $\varphi_1$  equals to  $\varphi_2$ , and fails (i.e., equals to  $\perp$ ) otherwise. We first need to define *definedness*  $[\varphi]$ , which is a predicate pattern that states that  $\varphi$  is *defined*, i.e.,  $\varphi$  is matched by at least one element:  $\varphi$  is not  $\perp$ .

**Definition 3.** Consider a symbol  $[\_]$   $\in \Sigma$ , called the definedness symbol. We write  $[\varphi]$  for the application  $[\_] \varphi$ . In addition, we define the following axiom:

$$\text{(Definedness)} \quad [x] \tag{5}$$

(Definedness) states that any element  $x$  is *defined*. Using the definedness symbol, we can define many important mathematical instruments, including equality, as the following notations:

$$\begin{aligned} [\varphi] &\equiv \neg[\neg\varphi] && // \text{Totality} & \varphi_1 = \varphi_2 &\equiv [\varphi_1 \leftrightarrow \varphi_2] && // \text{Equality} \\ \varphi_1 \subseteq \varphi_2 &\equiv [\varphi_1 \rightarrow \varphi_2] && // \text{Inclusion} & x \in \varphi &\equiv [x \wedge \varphi] && // \text{Membership} \end{aligned}$$

[22, Section 5.1] shows that the above indeed capture the intended semantics.

**Theory of Sorts.** Matching logic is not sorted, but  $\mathbb{K}$  is. To compile  $\mathbb{K}$  into matching logic, we need a systematic way to dealing with sorts. We follow the “sort-as-predicate” paradigm to handle sorts and sorted functions in matching logic, following [6,4]. The main idea is to define a symbol  $[\_ ] \in \Sigma$ , called the *inhabitant symbol*, and use the *inhabitant pattern*  $[\_ ]s$  (abbreviated for the application  $[\_ ]s$ ) to represent the *inhabitant set* of sort  $s$ . For example, to define a sort  $Nat$ , we define a corresponding symbol  $Nat$  that represents the sort name, and use  $[\_ ]Nat$  to represent the set of all natural numbers.

*Sorted functions* can be axiomatized as special matching logic symbols. For example, the successor function  $succ$  of natural numbers is a symbol with axiom:

$$\forall x. x \in [\_ ]Nat \rightarrow \exists y. y \in [\_ ]Nat \wedge succ\ x = y \quad (6)$$

In other words, for any  $x$  in the inhabitant set of  $Nat$ , there exists a  $y$  in the inhabitant set of  $Nat$  such that  $succ\ x$  equals to  $y$ . Thus,  $succ$  is a sorted function from  $Nat$  to  $Nat$ .

**Theory of Rewriting.** Recall that in  $\mathbb{K}$ , the formal language semantics is defined using rewrite rules, which essentially define a *transition system* over computation configurations. In matching logic, a transition system can be captured by only one symbol  $\bullet \in \Sigma$ , called *one-path next*, with the intuition that for any configuration  $\gamma$ ,  $\bullet\gamma$  is matched by all configurations that can go to  $\gamma$  in one step. In other words,  $\gamma$  is reached on *one-path* in the *next* configuration.

Program execution is the reflexive and transitive closure of one-path next. Formally, we define program execution (i.e., rewriting) as follows:

$$\begin{aligned} \diamond\varphi &\equiv \mu X. \varphi \vee \bullet X && // \text{Eventually; equals to } \varphi \vee \bullet\varphi \vee \bullet\bullet\varphi \vee \dots \\ \varphi_1 \Rightarrow \varphi_2 &\equiv \varphi_1 \rightarrow \diamond\varphi_2 && // \text{Rewriting} \end{aligned}$$

## 5.2 Kore: The Intermediate Between $\mathbb{K}$ and Matching Logic

The  $\mathbb{K}$  compilation tool `kompile` (explained shortly) is what compiles a  $\mathbb{K}$  language definition into a matching logic theory  $\Gamma^L$ , written in a formal language called Kore. For legacy reasons, the Kore language is not the same as the syntax of matching logic (Definition 1), but an axiomatic extension with equality, sorts, and rewriting. Thus, to formalize  $\Gamma^L$  in proof objects, we need to (1) formalize the matching logic theories of equality, sorts, and rewriting; and (2) automatically translate Kore definitions into the corresponding matching logic theories. Figure 7 shows the 2-phase translation from  $\mathbb{K}$  to matching logic, via Kore.

*Phase 1: From  $\mathbb{K}$  to Kore.* To compile a  $\mathbb{K}$  definition such as `two-counters.k` in Figure 3, we pass it to the  $\mathbb{K}$  compilation tool `kompile` as follows:

```
$ kompile two-counters.k
```

The result is a compiled Kore definition `two-counters.kore`. We show the auto-generated Kore axiom in Figure 7 that corresponds to the rewrite rule in Equation (3). As we can see, Kore is a much lower-level language than  $\mathbb{K}$ , where the programming language concrete syntax and  $\mathbb{K}$ 's front end syntax are parsed and replaced by the abstract syntax trees, represented by the constructor terms.

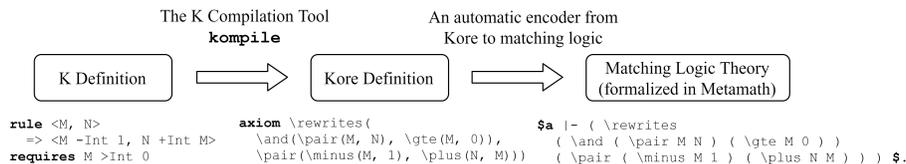


Fig. 7: Automatic translation from  $\mathbb{K}$  to matching logic, via Kore

*Phase 2: From Kore to Matching Logic.* We develop an automatic encoder that translates Kore syntax into matching logic patterns. Since Kore is essentially the theory of equality, sorts, and rewriting, we can define the syntactic constructs of the Kore language as *notations*, using the basic theories in Section 5.1.

## 6 Generating Proof Objects for Program Execution

In this section, we discuss how to generate proof objects for program execution, based on the formalization of matching logic and  $\mathbb{K}$ /Kore in Sections 4 and 5. The key step is to generate proof objects for *one-step executions*, which are then put together to build the proof objects for multi-step executions using the transitivity of the rewriting relation. Thus, we focus on the process of generating proof objects for one-step executions from the proof parameters provided by  $\mathbb{K}$ .

### 6.1 Problem Formulation

Consider the following  $\mathbb{K}$  definition that consists of  $K$  (conditional) rewrite rules:

$$S = \{t_k \wedge p_k \Rightarrow s_k \mid k = 1, 2, \dots, K\}$$

where  $t_k$  and  $s_k$  are the left- and right-hand sides of the rewrite rule, respectively, and  $p_k$  is the rewriting condition. Consider the following execution trace:

$$\varphi_0, \varphi_1, \dots, \varphi_n \tag{7}$$

where  $\varphi_0, \dots, \varphi_n$  are snapshots. We let  $\mathbb{K}$  generate the following proof parameter:

$$\Theta \equiv (k_0, \theta_0), \dots, (k_{n-1}, \theta_{n-1}) \tag{8}$$

where for each  $0 \leq i < n$ ,  $k_i$  denotes the rewrite rule that is applied on  $\varphi_i$  ( $1 \leq k_i \leq K$ ) and  $\theta_i$  denotes the corresponding substitution such that  $t_{k_i} \theta_i = \varphi_i$ .

As an example, the rewrite rule of `TWO-COUNTERS`, restated below:

$$\langle m, n \rangle \Rightarrow \langle m - 1, n + m \rangle \quad \text{if } m > 0 \quad // \text{ Same as Equation (3)}$$

has the left-hand side  $t_k \equiv \langle m, n \rangle$ , the right-hand side  $s_k \equiv \langle m - 1, n + m \rangle$ , and the condition  $p_k \equiv m \geq 0$ . Note that the right-hand side pattern  $s_k$  contains the arithmetic operations “+” and “-” that can be further evaluated to a value, if concrete instances of the variables  $m$  and  $n$  are given. Generally speaking, the right-hand side of a rewrite rule may include (built-in or user-defined) functions that are not constructors and thus can be further evaluated. We call such evaluation process a *simplification*.

## 6.2 Applying Rewrite Rules and Applying Simplifications

In the following, we list all proof objects for one-step executions.

$$\begin{array}{ll}
\Gamma^L \vdash \varphi_0 \Rightarrow s_{k_0} \theta_0 & // \text{ by applying } t_{k_0} \wedge p_{k_0} \Rightarrow s_{k_0} \text{ using } \theta_0 \\
\Gamma^L \vdash s_{k_0} \theta_0 = \varphi_1 & // \text{ by simplifying } s_{k_0} \theta_0 \\
\cdots & \\
\Gamma^L \vdash \varphi_{n-1} \Rightarrow s_{k_{n-1}} \theta_{n-1} & // \text{ by applying } t_{k_{n-1}} \wedge p_{k_{n-1}} \Rightarrow s_{k_{n-1}} \text{ using } \theta_{n-1} \\
\Gamma^L \vdash s_{k_{n-1}} \theta_{n-1} = \varphi_n & // \text{ by simplifying } s_{k_{n-1}} \theta_{n-1}
\end{array}$$

As we can see, there are two types of proof objects: one that proves the results of *applying rewrite rules* and one that *applies simplification*.

**Applying Rewrite Rules.** The main steps in proving  $\Gamma^L \vdash \varphi_i \Rightarrow s_{k_i} \theta_i$  are (1) to *instantiate* the rewrite rule  $t_{k_i} \wedge p_{k_i} \Rightarrow s_{k_i}$  using the substitution

$$\theta_i = [c_1/x_1, \dots, c_m/x_m]$$

given in the proof parameter, and (2) to show that the (instantiated) rewriting condition  $p_{k_i} \theta_i$  holds. Here,  $x_1, \dots, x_m$  are the variables that occur in the rewrite rule and  $c_1, \dots, c_m$  are terms by which we instantiate the variables. For (1), we need to first prove the following lemma, called (Functional Substitution) in [5], which states that  $\forall$ -quantification can be instantiated by functional patterns:

$$\frac{\forall \vec{x}. t_{k_i} \wedge p_{k_i} \Rightarrow s_{k_i} \quad \exists y_1. \varphi_1 = y_1 \quad \cdots \quad \exists y_m. \varphi_m = y_m}{t_{k_i} \theta_i \wedge p_{k_i} \theta_i \Rightarrow s_{k_i} \theta_i} \quad y_1, \dots, y_m \text{ fresh}$$

Intuitively, the premise  $\exists y_1. \varphi_1 = y_1$  states that  $\varphi_1$  is a functional pattern because it equals to some element  $y_1$ .

If  $\Theta$  in Equation (8) is the correct proof parameter,  $\theta_i$  is the correct substitution and thus  $t_{k_i} \theta_i \equiv \varphi_i$ . Therefore, to prove the original proof goal for one-step execution, i.e.  $\Gamma^L \vdash \varphi_i \Rightarrow s_{k_i} \theta_i$ , we only need to prove that  $\Gamma^L \vdash p_{k_i} \theta_i$ , i.e., the rewriting condition  $p_{k_i}$  holds under  $\theta_i$ . This is done by *simplifying*  $p_{k_i} \theta_i$  to  $\top$ , discussed together with the simplification process in the following.

**Applying Simplifications.**  $\mathbb{K}$  carries out simplification exhaustively before trying to apply a rewrite rule, and simplifications are done by applying (oriented) equations. Generally speaking, let  $s$  be a functional pattern and  $p \rightarrow t = t'$  be a (conditional) equation, we say that  $s$  can be *simplified* w.r.t.  $p \rightarrow t = t'$ , if there is a sub-pattern  $s_0$  of  $s$  (written  $s \equiv C[s_0]$  where  $C$  is a context) and a substitution  $\theta$  such that  $s_0 = t\theta$  and  $p\theta$  holds. The resulting *simplified pattern* is denoted  $C[t'\theta]$ . Therefore, a proof object of the above simplification consists of two proofs:  $\Gamma^L \vdash s = C[t'\theta]$  and  $\Gamma^L \vdash p\theta$ . The latter can be handled recursively, by simplifying  $p\theta$  to  $\top$ , so we only need to consider the former.

The main steps of proving  $\Gamma^L \vdash s = C[t'\theta]$  are the following:

1. to find  $C$ ,  $s_0$ ,  $\theta$ , and  $t = t'$  in  $\Gamma^L$  such that  $s \equiv C[s_0]$  and  $s_0 = t\theta$ ; in other words,  $s$  can be simplified w.r.t.  $t = t'$  at the sub-pattern  $s_0$ ;
2. to prove  $\Gamma^L \vdash s_0 = t'\theta$  by instantiating  $t = t'$  using the substitution  $\theta$ , using the same (Functional Substitution) lemma as above;
3. to prove  $\Gamma^L \vdash C[s_0] = C[t']$  using the transitivity of equality.

Finally, we repeat the above one-step simplifications until no sub-patterns can be simplified further. The resulting proof objects are then put together by the transitivity of equality.

## 7 Discussion on Implementation

As discussed in Section 2, a complete proof object for program execution (i.e.,  $\Gamma^L \vdash \varphi_{init} \Rightarrow \varphi_{final}$ ) consists of (1) the formalization of matching logic and its basic theories; (2) the formalization of  $\Gamma^L$ ; and (3) the proofs of one-step and multi-step program executions. In our implementation, (1) is developed manually because it is fixed for all programming languages and program executions. (2) and (3) are automatically generated by the algorithms in Section 6.

During the (manual) development of (1), we needed to prove many basic matching logic (meta-)theorems as lemmas, such as (Functional Substitution) in Section 6.2. To ease the manual work, we developed an *interactive theorem prover* (ITP) for matching logic, which allows us to carry out higher-level interactive proofs that are later automatically translated into the lower-level Metamath proofs. We show the highlights of our ITP for matching logic in Section 7.1.

In Section 7.2, we discuss the main limitations of our current preliminary implementation. These limitations are planned to be addressed in future work.

### 7.1 An Interactive Theorem Prover for Matching Logic

Metamath proofs are low-level and not human readable (see, e.g., the proof of  $\vdash \varphi \rightarrow \varphi$  in Figure 6). Metamath has its own interactive theorem prover (ITP), but it is for general purposes and does not have specific support for matching logic. Therefore, we developed a new ITP for matching logic that has the following characteristic features:

- Our ITP understands the syntax of matching logic patterns and has proof tactics to *desugar* notations in the proof goals;
- Our ITP has an automatic proof tactic for propositional tautologies, based on the *resolution* method;
- Our ITP allows *dynamic proofs*, meaning that new lemmas can be dynamically added during an interactive proof; this makes our ITP easier to use.

When an interactive proof is finished, our ITP will translate the higher-level proof tactics into real Metamath formal proofs, and thus ease the manual development. It is not our interest to fully introduce ITP in this paper, as more detail about the ITP is to be found in future publications.

## 7.2 Limitations and Threats to Validity

We discuss the trust base of the autogenerated proof objects by pointing out the main threats to validity, caused by the limitations of our preliminary implementation. It should be noted that these limitations are about the implementation, and *not* our approach. We shall address these limitations in future work.

**Limitation 1: Need to trust Kore.** Our current implementation is based on the existing  $\mathbb{K}$  compilation tool `kompile` that compiles  $\mathbb{K}$  into Kore definitions. Recall that Kore is a (legacy) formal language with built-in support for equality, sorts, and rewriting, and thus is different (and more complex) than the syntax of matching logic. By using Kore as the intermediate between  $\mathbb{K}$  and matching logic (Figure 7), we need to trust Kore and the  $\mathbb{K}$  compilation tool `kompile`.

In the future, we will eliminate Kore entirely from the picture and formalize  $\mathbb{K}$  *directly*. To do that, we need to formalize the “front end matters” of  $\mathbb{K}$ , such as concrete programming language syntax and  $\mathbb{K}$  attributes, currently handled by `kompile`. That is, we need to formalize and generate proof objects for `kompile`.

**Limitation 2: Need to trust domain reasoning.**  $\mathbb{K}$  has built-in support for domain reasoning such as integer arithmetic. Our current proof objects do not include the formal proofs of such domain reasoning, but instead regard them as assumed lemmas. In the future, we will incorporate the existing research on generating proof objects for SMT solvers [1] into our implementation, in order to generate proof objects also for domain reasoning; see also Section 9.

**Limitation 3: Do not support more complex  $\mathbb{K}$  features.** Our current implementation only supports the core  $\mathbb{K}$  features of defining programming language syntax and of defining formal semantics as rewrite rules. Some more complex features are not supported; the main ones are (1) the `[strict]` attributes that specify evaluation orders; and (2) the use of built-in collection datatypes, such as lists, sets, and maps.

To support (1), we should handle the so-called *heating/cooling rules* that are autogenerated rewrite rules that implement the specified evaluation orders. Our current implementation does not support these heating/cooling rules because they are conditional rules, and their conditions are those that state that an element is *not* a computation result. To prove such conditions, we need additional constructors axioms for the sorts/types that represent results of computation. To support (2), we should extend our algorithms in Section 6 with *unification* modulo these collection datatypes.

## 8 Evaluation

In this section, we evaluate the performance of our implementation and discuss the experiment results, summarized in Table 1. We use two sets of benchmarks.

Table 1: Performance of proof generation/checking (time measured in seconds).

programs	proof generation			proof checking			proof size	
	sem	rewrite	total	logic	task	total	kLOC	MB
10.two-counters	5.95	12.19	18.13	3.26	0.19	3.44	963.8	77
20.two-counters	6.31	24.33	30.65	3.41	0.38	3.79	1036.5	83
50.two-counters	6.48	73.09	79.57	3.52	0.98	4.50	1259.2	100
100.two-counters	6.75	177.55	184.30	3.50	2.10	5.60	1635.6	130
add8	11.59	153.34	164.92	3.40	3.09	6.48	1986.8	159
factorial	3.84	34.63	38.46	3.57	0.90	4.47	1217.9	97
fibonacci	4.50	12.51	17.01	3.44	0.21	3.65	971.7	77
benchexpr	8.41	53.22	61.62	3.61	0.80	4.41	1191.3	95
benchsym	8.79	47.71	56.50	3.53	0.72	4.25	1163.4	93
benchtree	8.80	26.86	35.66	3.47	0.32	3.80	1021.5	81
langton	5.26	23.07	28.33	3.46	0.40	3.86	1048.0	84
mul8	14.39	279.97	294.36	3.48	7.18	10.66	3499.2	280
revelt	4.98	51.83	56.81	3.35	1.10	4.45	1317.4	105
revnat	4.81	123.44	128.25	3.37	5.28	8.65	2691.9	215
tautologyhard	5.16	400.89	406.05	3.55	14.50	18.04	6884.7	550

The first is our running example `TWO-COUNTERS` with different inputs (10, 20, 50, and 100). The second is REC [11], which is a popular performance benchmark for rewriting engines. We evaluate both the performance of proof object *generation* and that of proof *checking*. Our implementation can be found in [16] and [3].

The main takeaways of our experiments are:

1. Proof checking is efficient and takes a few seconds; in particular, the *task-specific* checking time is often less than one second (“task” column in Table 1).
2. Proof object generation is slower and takes several minutes.
3. Proof objects are huge, often of millions LOC (wrapped at 80 characters).

**Proof Object Generation.** We measure the proof object generation time as the time to generate complete proof objects following the algorithms in Section 6, from the compiled language semantics (i.e., Kore definitions) and proof parameters. As shown in Table 1, proof generation takes around 17–406 seconds on the benchmarks, and the average is 107 seconds.

Proof object generation can be divided into two parts: that of the language semantics  $\Gamma^L$  and that of the (one-step and multi-step) program executions. Both parts are shown in Table 1 under columns “sem” and “rewrite”, respectively. For the same language, the time to generate language semantics  $\Gamma^L$  is the same (up to experimental error). The time for executions is linear to the number of steps.

**Proof Checking.** Proof checking is efficient and takes a few seconds on our benchmarks. We can divide the proof checking time into two parts: that of the

logical foundation and that of the actual program execution tasks. Both parts are shown in Table 1 under columns “logic” and “task”. The “logic” part includes formalization of matching logic and its basic theories, and thus is *fixed* for any programming language and program and has the same proof checking time (up to experimental error). The “task” part includes the language semantics and proof objects for the one-step and multi-step executions. Therefore, the time to check the “task” part is a *more valuable and realistic* measure, and according to our experiments, it is often less than 1 second, making it acceptable in practice.

As a pleasant surprise, the time for “task-specific” proof checking is roughly the same as the time that it takes  $\mathbb{K}$  to parse and execute the programs. In other words, there is *no significant performance difference* on our benchmarks between running the programs directly in  $\mathbb{K}$  and checking the proof objects.

There exists much potential to optimize the performance of proof checking and make it even faster than program execution. For example, in our approach proof checking is an *embarrassingly parallel problem*, because each meta-theorems can be proof-checked entirely independently. Therefore, we can significantly reduce the proof checking time by running multiple checkers in parallel.

## 9 Related Work

The idea of using proof generation to address the functional correctness of complicated systems has been introduced a long time ago.

Interactive theorem provers such as Coq [19] and Isabelle [26] are often used to formalize programming language semantics and to reason about program properties. These provers often provide a high-level proof script language that allows the users to develop human-readable proofs, which are then automatically translated into lower-level proof objects that can be checked by the corresponding proof checkers. For example, the proof objects of Coq are of the form  $t : t'$ , where  $t'$  is a term that represents the proposition to be proved and  $t$  represents a formal proof. The typing claim  $t : t'$  can then be proof-checked by a proof checker that implements the typing rules of the calculus of inductive constructions (CIC) [8], which is the logical foundation of Coq.

There are two main differences between provers such as Coq and our technique. Firstly, Coq is not regarded as a language framework in the sense of Figure 1 because no language tools are autogenerated from the formal semantics. In our case, we need to be able to handle the correctness of individual tasks on a case-by-case basis to reduce the complexity. Secondly, Coq proof checking is based on CIC, which is arguably more complex than matching logic—the logical foundation of  $\mathbb{K}$  as demonstrated in this paper. Indeed, the formalization of matching logic requires only 245 LOC which we display entirely in [16].

Another application of proof generation is to ensure the correctness of SMT solvers. These are popular tools to check the satisfiability of FOL formulas, written in a formal language containing interpreted functions and predicates. SMT solvers often implement complex data structures and algorithms, putting their correctness at risk. There is recent work such as [1] studying proof generation

for SMT solvers. The research has been incorporated in theorem provers such as Lean, which attempts to bridge the gap between SMT reasoning and proof assistants more directly by building a proof assistant with efficient and sophisticated built-in SMT capabilities. As discussed in Section 7, our current implementation does not generate proofs for domain reasoning. So, we plan to incorporate the above SMT proof generation work into our future implementation.

## 10 Conclusion

We propose an innovative approach based on proof generation. The key idea is to generate proof objects as *proof certificates* for each individual task that the language tools conduct, on a case-by-case basis. This way, we avoid formally verifying the entire framework, which is practically impossible, and thus can make the language framework both *practical* and *trustworthy*.

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